

Fig. 1

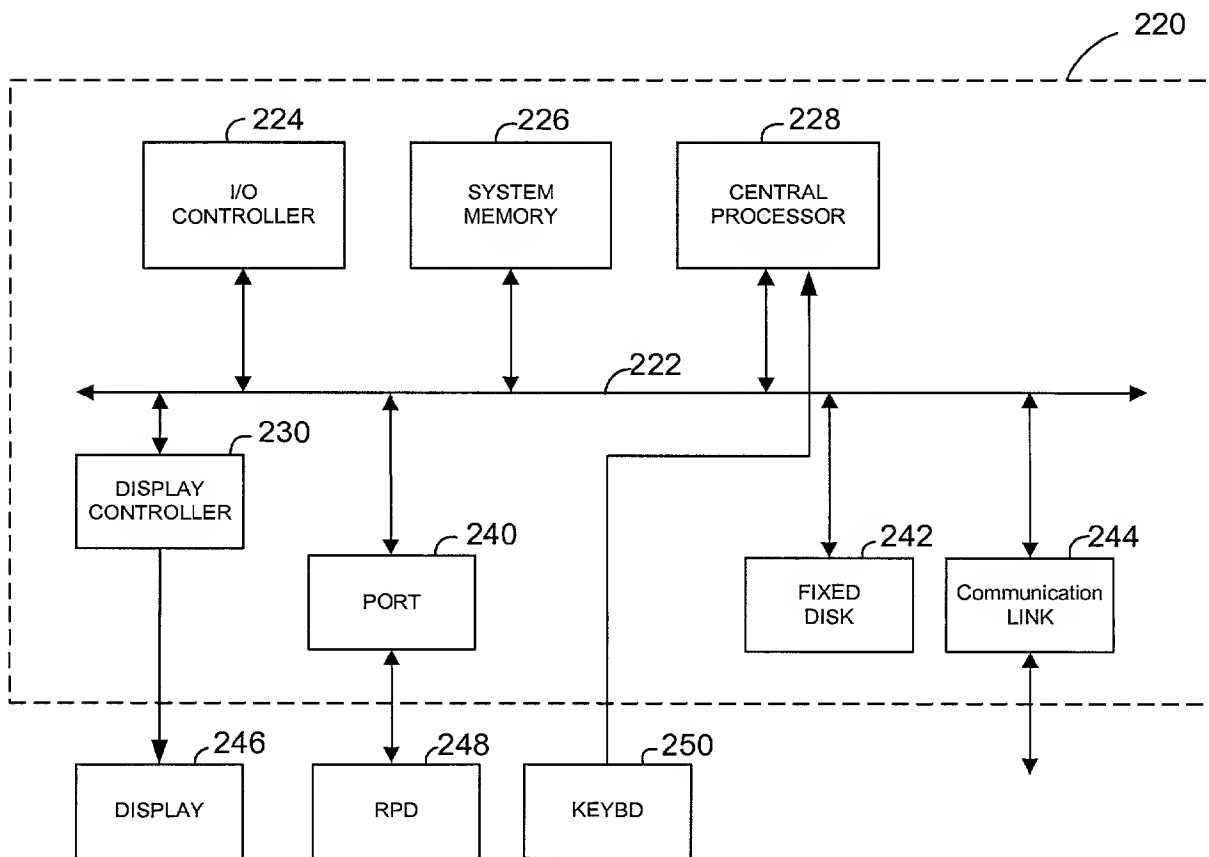


Fig. 2

TO OTHER MATRICES 150

(INCLUDING CONTROLLER 120 AND MEMORY 140)

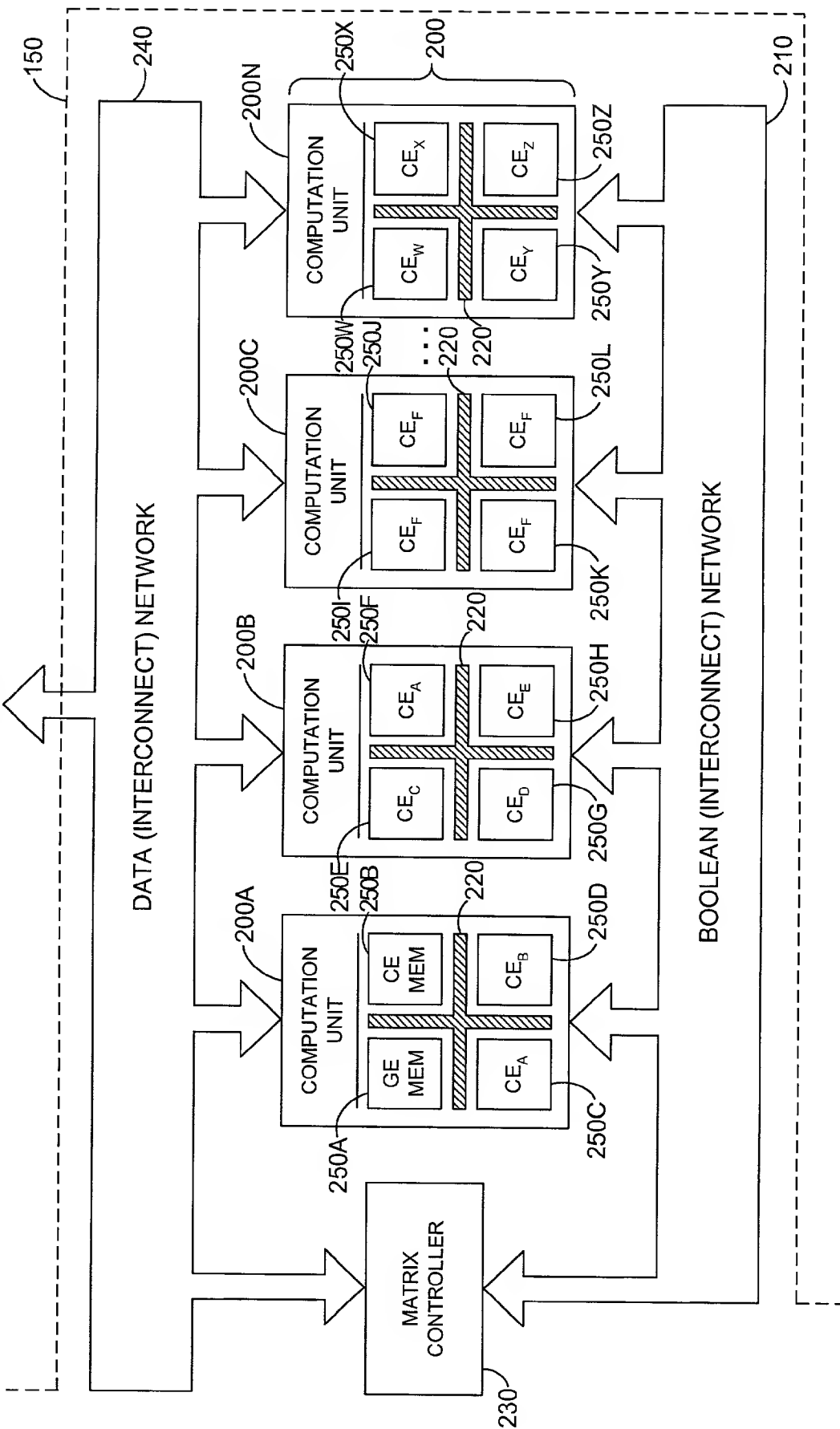


FIG. 3